

REMARKS/ARGUMENTS

Claims 1-26 are pending in this application. Claims 1 and 15 are independent claims.

Claim Rejections – 35 USC § 102

Claim 1 was rejected under 35 U.S.C. § 102(b) as being anticipated by Nagashige et al. (“Nagashige”, U.S. Patent Number 5,313,588). Applicant respectfully traverses this rejection.

Anticipation requires the disclosure in a single prior art reference of each element of the claim under consideration. *W.L. Gore & Assocs. v. Garlock*, 721 F.2d 1540, 220 USPQ 303 (Fed. Cir. 1983), cert. denied, 469 U.S. 851 (1984). Further, “anticipation requires the presence in a single prior art reference disclosure of each and every element of the claimed invention, arranged as in the claim.” *Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick Co.*, 730 F.2d 1452, 221 USPQ 481, 485 (Fed. Cir. 1984) (citing *Connell v. Sears, Roebuck & Co.*, 722 F.2d 1542, 220 USPQ 193 (Fed. Cir. 1983)) (emphasis added).

Independent Claim 1 recites a “bus operation information structure.” In rejecting Claim 1, the Patent Office has analogized a command in Nagashige to a “bus operation information structure” (Office Action, page 2). Applicant respectfully disagrees.

As indicated in the instant Patent Application, “[t]he bus operation information structures 112 include instructions and data with which a sequencer 116 within the SCSI host adapter 104 performs the process (e.g. read data, write data, etc.) requested by the CPU 110” (emphasis added) (Specification, page 1, lines 24-26). In other words, a “bus operation information structure” is more than an instruction (e.g., a write command, a read command, and the like); instead, it includes *both* a command and data to be transferred.

Therefore, Nagashige fails to teach, disclose, or suggest the element of “bus operation information structure,” as claimed in Claim 1. Accordingly, the rejection should be withdrawn, and Claim 1 should be allowed.

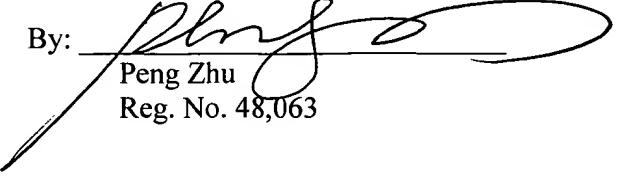
CONCLUSION

In light of the foregoing, Applicant respectfully requests that a timely Notice of Allowance be issued in the case.

Respectfully submitted on behalf of
LSI Logic Corporation,

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By:


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